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Docket: 0756-1930

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of)

Shunpei YAMAZAKI et al.)

Serial No. 09/239,948) Art Unit: 2811

Filed: 01/29/99) Examiner: G. Munson

For: LASER PROCESSING METHOD, METHOD) *#6/c*

FOR FORMING A FLASH MEMORY,) *3-2400*

INSULATED GATE SEMICONDUCTOR) *Payton*

DEVICE AND METHOD FOR FORMING)

THE SAME)

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231 on 3-10-2000.

SUPPLEMENTAL AMENDMENT

Honorable Assistant Commissioner of Patents

Washington, D.C. 20231

Sir:

Please amend the above-identified application as follows:

In the claims:

Please add the following new claims:

Sub E²
017
11.
--17. A semiconductor memory device comprising:
a first memory comprising:
a first floating gate formed over a semiconductor substrate;

03/17/2000 SLURMS 00000025 09239948 2412.00 OP 936.00 OP
01 FC:103
02 FC:102